

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshinori Hino et al.

Art Unit : 2826

Serial No.: 10/076,154

Filed : February 14, 2002 Examiner: Tan N. Tran

Title

: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 23, 2003

Chris T. Mizumoto Reg. No. 42,899

Fish & Richardson P.C. 45 Rockefeller Plaza, Suite 2800 New York, New York 10111 Telephone: (212) 765-5070 Facsimile: (212) 258-2291

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			U.S. Pate	nt Documents			
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD				1		
	AE						
	AF						
	AG						
	AH						
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Foreign Patent Documents or Published Foreign Patent Applications								
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Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL							
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	Other Documents (include Author, Title, Date, and Place of Publication)				
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Initial	ID	Document			
	AQ	Taiwanese Office Action dated November 1, 2002			
	AR	Clement H. Wann, "A Comparative Study of Advanced MOSFET Concepts", IEE Transactions of Electric: Volume 43,(1996)			
	AS	"Phenomena in Graded Junction Devices"; Texas Instruments Incorporated			
	AT	McGraw-Hill, "ULSI Technology", (1996)			

Examiner Signature	Date Considered				
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with					
next communication to applicant.					